

CLAIM AMENDMENTS

Claims 1-3 (Cancelled).

4. (Previously Presented) A semiconductor integrated circuit comprising:
a main circuit having an active element and a first pad;
a circuit block including a passive element;
a second pad connected to said circuit block; and
a wire connecting said first pad to said second pad, wherein

 said main circuit includes an input terminal and an output terminal and said active element and said first pad are located between said input terminal and said output terminal,

 said circuit block includes an interconnect connected to said second pad,
and

 said wire and said interconnect have lengths totaling one-fourth of a wavelength of a signal input to said input terminal.

Claims 5-15 (Cancelled).